AMENDMENTS TO THE SPECIFICATION

Please amend the third paragraph on page 14 as follows:

The comparator 28 reflects the voltage data V11, V12, and V13 respectively in the triangular wave signal 45 which is an output of the triangular wave counter 26, in the respective time periods Δt of the reflection timing 42, 43, and 44, and outputs a PWM signal to the switching circuit ~~13~~12. Herein, each time period Δt of the reflection timing 42, 43, and 44 indicates a period during which a predetermined number (three in the example of Fig. 6) of vertices of the triangular wave signal 45 has passed, and the start point and the end point thereof synchronize to the vertices of the triangular wave signal 45.

Please amend the third paragraph on page 15 as follows:

On the other hand, in the embodiment, in the calculation period (time period ΔT) in which the phase change amount exceeds, for example, a predetermined value as shown in Fig. 7(2), only the phase is divided into three parts, and three output-voltage command values are obtained one by one in each time period ΔT/3, such as V1 (θ1), V2 (θ2), and V3 ~~(θ23)~~ (θ3), and a PWM signal is generated for each value. Therefore, even if the output frequency becomes high and the calculation period becomes relatively long, the steps in the staircase-shaped waveform can be made smaller, which allows the waveform to approach a smoother sine wave.